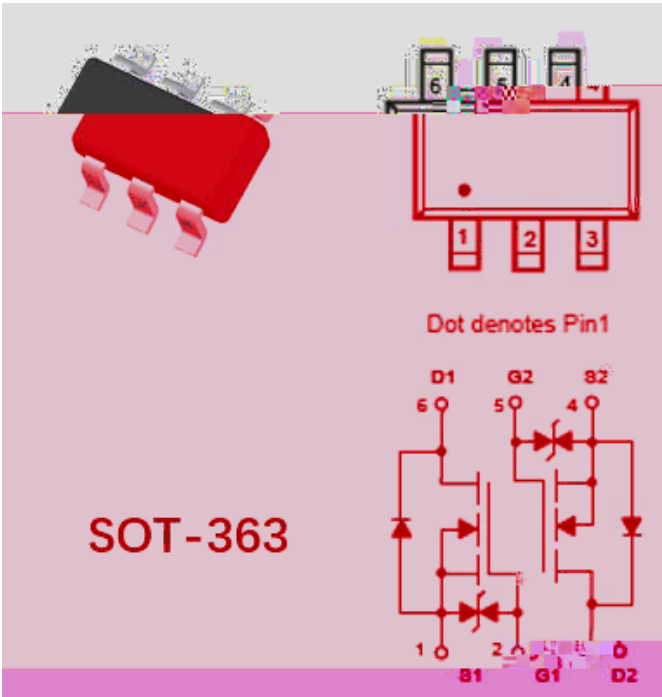




(at $V_{GS}=10V$) 2.5ohm
 χ $R_{DS(ON)}$ (at $V_{GS}=4.5V$) 3.0ohm
 χ ESD Protected Up to 2.0KV (HBM)

General Description

- χ Trench Power MV MOSFET technology
- χ Voltage controlled small signal switch
- χ Low input Capacitance
- χ Fast Switching Speed
- χ Low Input / Output Leakage
- χ Part no. with suffix $\phi Q\phi$ means AEC-Q101 qualified

Applications

- χ Battery operated systems
- χ Solid-state relays
- χ Direct logic-level interface TTL/CMOS

Ω Absolute Maximum Ratings (T Ö

			Limit	Unit
Drain-source Voltage		V_{DS}	60	V
Gate-source Voltage		V_{GS}	±20	V
Drain Current	$T_A=25^\circ C$ @ Steady State	I_D	300	mA
	$T_A=70^\circ C$ @ Steady State		240	
Pulsed Drain Current ^A		I_{DM}	1.5	A
Total Power Dissipation @ $T_A=25^\circ C$		P_D	300	mW
Thermal Resistance Junction-to-Ambient @ Steady State ^B		$R_{\theta JA}$	416	/ W
Junction and Storage Temperature Range		T_J, T_{STG}	-55 +150	

Ω Ordering Information (Example)

PREFERRED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
2N7002KCDWQ	F2	72KC	3000	30000	120000	7ø reel



Ω Electrical Characteristics (T_J=25

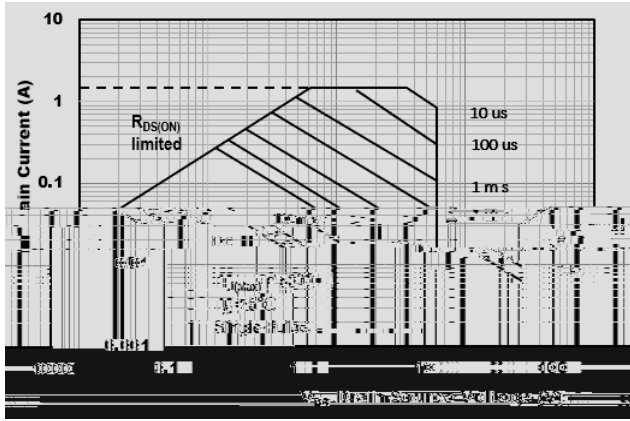


Figure7. Safe Operation Area

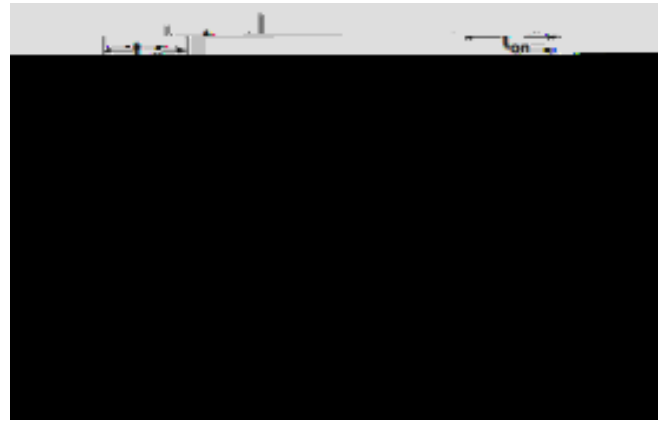
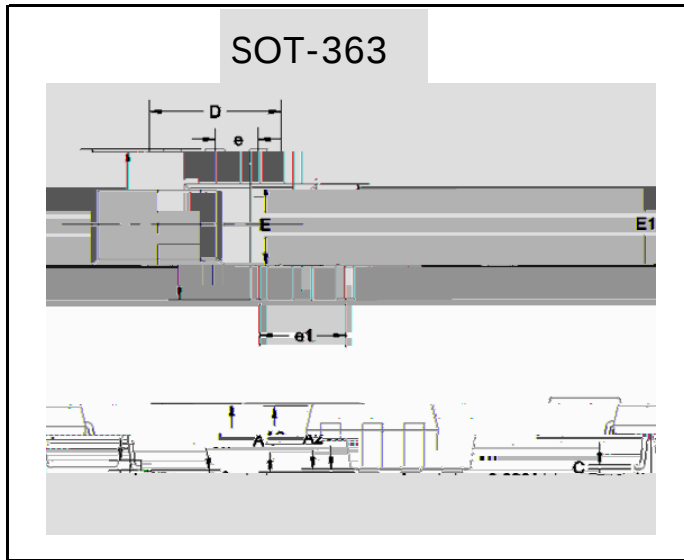


Figure8. Switching wave



2N7002KCDWQ

ΩSOT-363 Package Outline Dimensions



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.035	0.043	0.90	1.10	
A1	0.000	0.004	0.00	0.10	
A2	0.035	0.039	0.90	1.00	
b	0.006	0.014	0.15	0.35	
c	0.002	0.010	0.05	0.25	
D	0.071	0.087	1.80	2.20	
E	0.045	0.053	1.15	1.35	
E1	0.085	0.096	2.15	2.45	
e	0.026Typ		0.65Typ		
e1	0.047	0.055	1.20	1.40	
L	0.021Typ		0.525Typ		
L1	0.010	0.018	0.26	0.46	
%	0Å	8Å	0Å	8Å	



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