

Digital Transistors (Built-in Resistors)

Features

- Epoxy meets UL-94 V-0 flammability rating
- Moisture Sensitivity Level 1
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors
- Surface mount package ideally Suited for Automatic Insertion
- PNP

Mechanical Data

- **Package:** SOT-23
- **Terminals:** Tin plated leads, solderable per J-STD-002 and JESD22-B102
- **Marking:** 54

■Equivalent circuit



■Maximum Ratings (Ta=25 Unless otherwise specified)

ITEM	SYMBOL	UNIT	VALUE
Supply Voltage	V_{CC}	V	-50
Input Voltage	V_{IN}	V	-40 to +6
Output Current	I_O	mA	-100
Power Dissipation	P_D	mW	200
Junction Temperature	T_j		150
Storage Temperature	T_{STG}		-55 to +150



■Electrical Characteristics (Ta=25 unless otherwise specified)

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