

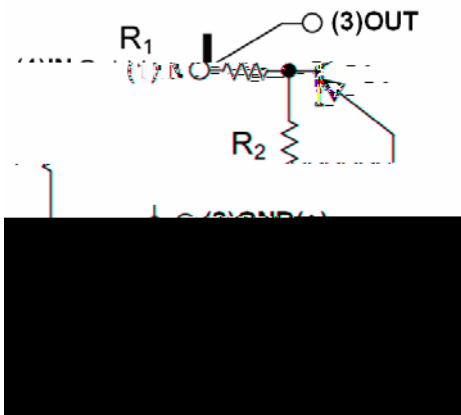
PNP Digital Transistors (Built-in Resistors)



Features

- Moisture Sensitivity Level 1
- Surface mount package
- Compliance with JEDEC JESD22-B102 and JESD22-B103
- MTin plated leads, solderable per MIL-STD-883C
- Pin 1: IN
- Pin 2: VCC
- Pin 3: OUT

Equivalent circuit



Maximum Ratings (Ta=25°C Unless otherwise specified)

ITEM	SYMBOL	UNIT	CONDITIONS	VALUE
Supply Voltage	V_{CC}	V		-50
Input Voltage	V_{IN}	V		-40 to 10
Output Current	I_o	mA		-100
Power Dissipation	P_D	mW		150
Junction Temperature	T_J			150
Storage Temperature	T_{STG}			-55 to +150



DTA144EE

Electrical Characteristics (Ta=25 unless otherwise specified)

ITEM	SYMBOL	UNIT	CONDITIONS	MIN	TYP	MAX
Input voltage	$V_{I(off)}$	V	$V_{CC}=-5V, I_o=-100\mu A$ $V_o=-0.3V, I_o=-2mA$	-0.5		
	$V_{I(on)}$	V				-3
Output voltage	$V_{O(on)}$	V	$I_o / I_i = -10mA / -0.5 mA$			-0.3
Input current	I_i	mA	$V_i = -5V$			-0.18
Output current	$I_{O(off)}$	μA	$V_{CC} = -50V, V_i = 0$			-0.5
DC current gain	G_i		$V_o = -5V, I_o = -5mA$	68		
Input resistance	R_i	k		32.9	47	61.1
Resistance ratio	R_2/R_1			0.8	1	1.2
Transition frequency	f_T	MHz	$V_o = -10V, I_o = 5mA$		250	

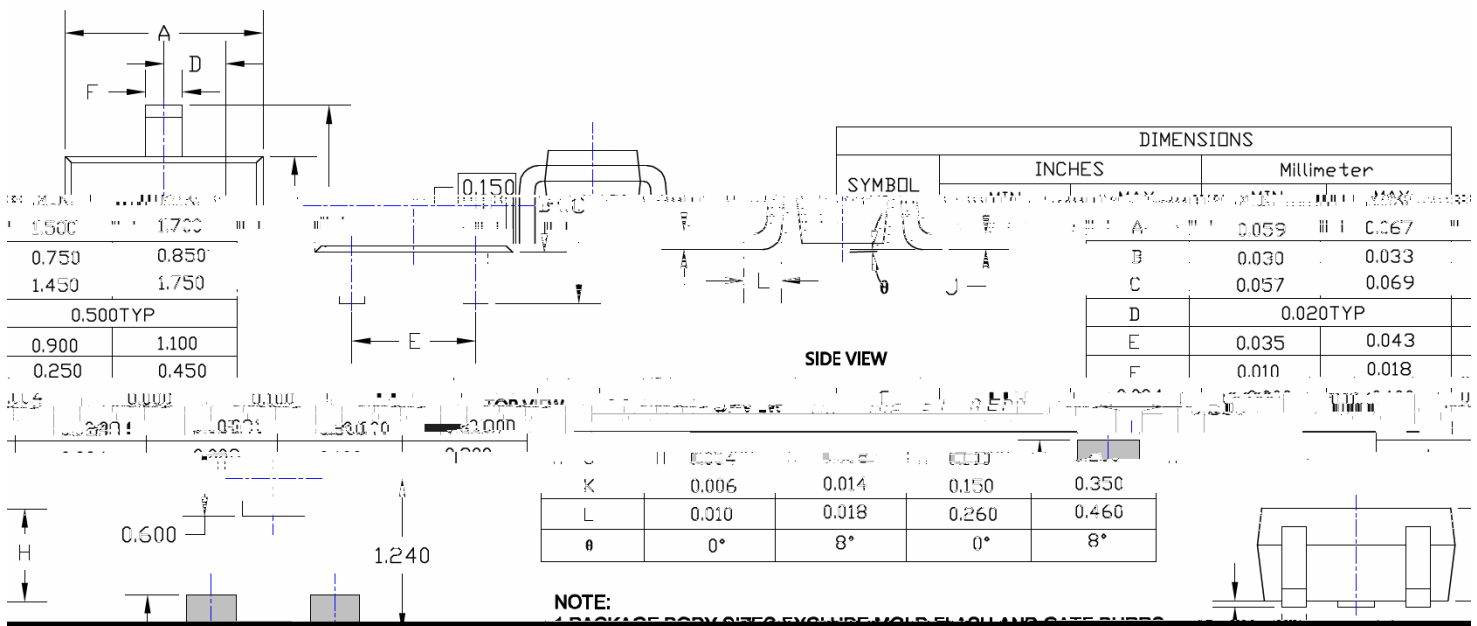
Ordering Information (Example)

Preferred P/N	Packing code	Unit Weight(G)	Minimum Package(Pcs)	Inner Box Quantity(Pcs)	Outer Carton Quantity(Pcs)	Delivery Mode
DTA144EE	F2	Approximate 0.0027	3000	30000	120000	7" reel



DTA144EE

SOT-523 Package Outline Dimensions



Note:

1. All dimensions are in millimeters (mm) unless otherwise specified.





Disclaimer

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