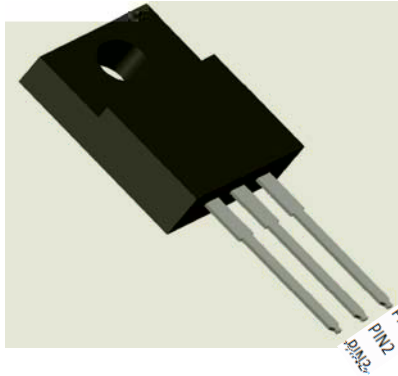




- : Tin plated leads, solderable per J-STD-002 and JESD22-B102
- : As marked

($T_a=25$ Unless otherwise specified)

Device marking code			MBR20150FCTS
Repetitive Peak Reverse Voltage	VRRM	V	150
Average Rectified Output Current @60Hz sine wave, R-load, $T_a=25$	IO	A	20
Surge(Non-repetitive)Forward Current @60Hz half sine-wave, 1 cycle, $T_a=25$	IFSM	A	130
Current Squared Time @1ms≤t≤8.3ms $T_j=25$	I ² t	A ² s	70
Storage Temperature	Tstg		-55 ~ +175
Junction Temperature	Tj		-55 ~ +175

$T_a=25$ Unless otherwise specified

Maximum instantaneous forward voltage drop per diode	VFM	V	IFM=10.0A	0.9
Maximum DC reverse current at rated DC blocking voltage per diode	I _{RRM1}	mA	V _{RM} =V _{RRM} $T_a=25$	0.1
	I _{RRM2}		V _{RM} =V _{RRM} $T_a=125$	20

Note1:Pulse test:300uS pulse width,1% duty cycle

Note2:Pulse test:pulse width 40mS



■ Ta=25 Unless otherwise specified

Thermal Resistance	Between junction and case	RθJ-C	W	4.0

■ (Example)

MBR20150FCTS	Approximate 1.6	50	1000	5000	Tube

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Dim	Min	Max
A	10.03	10.23
B	6.90	7.10
C	9.21	9.41
D	3.20	3.40
E	1.40	4.00
F	3.8	0.75
G	15.72	16.02
H	TYP 19.05	
I	TYP 3.18	
J	12.65	13.25
K	1.30	1.50
L	6.50	7.50
M	9.23	9.63
N	2.44	2.64
O	TYP 0.7	
P	3.35	3.55
Q	6.58	6.78
R	3.08	3.28
S	TYP 0.52	
T	2.75	2.95
U	0.40	0.60
V	4.50	4.90



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